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REUSABLE COMPLEX MULTI-BUS SYSTEM HARDWARE PROTOTYPE SYSTEM

ABSTRACT OF THE DISCLOSURE

A configurable and scaleable multi-bus platform
5 for developing, testing and/or debugging prototype
systems to be implemented in an integrated circuit
includes a backplane providing multiple busses.
Multiple system bus cards can be coupled to the
backplane, and each of the system bus cards includes
10 a system bus which is electrically coupled to at
least one bus on the backplane. The system bus cards
also include a bus infrastructure device providing
support logic for operating the system bus. Daughter
cards, containing master or slave devices for
15 particular design configurations, are coupleable to
the system bus cards in order to simulate a system
bus which will be implemented in the integrated
circuit. The backplane and system bus cards, as well
as other components, can be easily reused in other
20 projects for designing, testing and debugging other
integrated circuits.